

Electronic Patent Application Fee Transmittal

Application Number:	10538216
Filing Date:	09-Jun-2005
Title of Invention:	Vertical insulated gate transistor and manufacturing method
First Named Inventor/Applicant Name:	Jurriaan Schmitz
Filer:	Peter Zawilski
Attorney Docket Number:	NL02 1416

Filed as Large Entity

U.S. National Stage under 35 USC 371 Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
-------------	----------	----------	--------	----------------------

Basic Filing:

Pages:

Claims:

Independent claims in excess of 3	1614	1	200	200
-----------------------------------	------	---	-----	-----

Miscellaneous-Filing:

Petition:

Patent-Appeals-and-Interference:

Post-Allowance-and-Post-Issuance:

Extension-of-Time:

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension - 1 month with \$0 paid	1251	1	120	120
Miscellaneous:				
Total in USD (\$)				320